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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
 Stylesheet Version v18.0

Title of Invention	Managing Accesses in a Processor for Isolated Execution
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Application Number: 09/540611

Confirmation Number: 2172


First Named Applicant: Carl Ellison

Attorney Docket Number: 42P08112

Art Unit: 2134

Examiner: Ellen C. Tran

Search string: (3996449 or 4430709 or 4802084 or 4975836
 or 5187802 or 5230069 or 5295251 or 5361375
 or 5469557 or 5506975 or 5555385 or 5555414
 or 5574936 or 5956408 or 5978475 or 6044478
 or 6088262 or 6175924 or 6188257 or
 6275933).pn.



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APR 02 2004

Technology Center 2100

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

Examiner Name	Date
<i>ET</i>	10 AUG 04

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Title of Invention**Managing Accesses in a Processor for Isolated Execution**

Application Number: 09/540611

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First Named Applicant: Carl Ellison

MAY 11 2004

Attorney Docket Number: 42P08112

Art Unit: Unknown

Technology Center 2100

Examiner: Unknown Unknown

Search string: (4162536 or 4247905 or 4276594 or 4307447 or 4319323 or 4347565 or 4571672 or 4759064 or 4795893 or 4825052 or 4907270 or 4907272 or 4910774 or 5007082 or 5139760 or 5317705 or 5319760 or 5434999 or 5437033 or 5442645 or 5459869 or 5504922 or 5511217 or 5522075 or 5528231 or 5533126 or 5564040 or 5566323 or 5582717 or 5606617 or 5720609 or 5721222 or 5737604 or 5796835 or 5825875 or 5854913 or 5867577 or 5900606 or 5901225 or 5903752 or 5970147 or 6061794 or 6075938 or 6092095 or 6115816 or 6173417 or 6182089 or 6212635 or 6222923 or 6252650 or 6269392 or 6272637 or 6282650 or 6308270 or 6314409 or 6330670 or 6357004 or 6363485 or 6374286 or 6374317 or 6378068 or 6378072 or 6389537 or 6397242 or 6412035 or 6421702 or 6435416 or 6445797 or 6463535 or 6463537 or 6507904 or 6535988 or 6557104 or 6633963 or 6633981).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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65	1	4162536	1979-07-24	Morley, Richard E.			
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	3	4276594	1981-06-30	Morley, Richard E.			
	4	4307447	1981-12-22	Provanzano, Salvatore R., et al.			
	5	4319323	1982-03-09	Ermolovich, Thomas R., et al.			
	6	4347565	1982-08-31	Kaneda, Saburo , et al.			
	7	4571672	1986-02-18	Hatada, Minoru , et al.			
	8	4759064	1988-07-19	Chaum,			
	9	4795893	1989-01-03	Ugon, Michael			
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17	5319760	1994-06-07	Mason, Andrew H., et al.
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19	5437033	1995-07-25	Inoue, Taro , et al.
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21	5459869	1995-10-17	Spilo, Michael L.
22	5504922	1996-04-02	Seki, Yukihiro , et al.
23	5511217	1996-04-23	Nakajima, Atsushi , et al.
24	5522075	1996-05-28	Robinson, Paul T., et al.
25	5528231	1996-06-18	Patarin, Jacques
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27	5564040	1996-10-08	Kubals, Jeffrey P.
28	5566323	1996-10-15	Ugon, Michel
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30	5606617	1997-02-25	Brands, Stefanus A.
31	5720609	1998-02-24	Pfefferle, William C.
32	5721222	1998-02-24	Bernstein, Peter R., et al.
33	5737604	1998-04-07	Miller, David A., et al.
34	5796835	1998-08-18	Saada, Charles
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39	5901225	1999-05-04	Ireton, Mark A., et al.
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Remarks

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Signature

Examiner Name	Date
	10 Aug '04



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Managing Accesses in a Processor for Isolated Execution

Application Number: 09/540611



Confirmation Number: 2172

First Named Applicant: Carl Ellison

Attorney Docket Number: 42P08112

Art Unit: 2134

Examiner: Ellen Tran

Search string: (3699532 or 4207609 or 4319233 or 4403283
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US Patent Documents

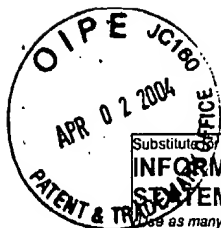
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Signature

Examiner Name	Date
<i>ET Lm</i>	<i>10 MAR '04</i>



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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number 09/540611

Filing Date March 31, 2000

First Named Inventor Ellison, Carl

Group Art Unit Unknown

Examiner Name Unknown

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Sheet 1 of 1

Attorney Docket No: 42P08112

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
✓	EP-EP0961193	12/01/1999	Laczko, Sr., Frank L.			
✓	EP-EP0965902	12/22/1999	Force, Gordon , et al.			
✓	EP-EP1085396	03/21/2001	Proudlar, Graeme J., et al.			
✓	WO-WO0021238	04/13/2000	Drews, Paul C.			
✓	WO-WO0163994	08/30/2001	Van Sant, Glen , et al.			
✓	WO-WO9524696	09/14/1995	Mooney, David M., et al.			
✓	WO-WO99/65579	12/23/1999	Bond, Eugene T.			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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EXAMINER

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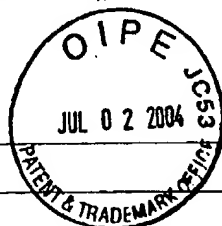
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Complete if Known

Application Number	09/540,611
Filing Date	March 31, 2000
First Named Inventor	Ellison, Carl
Group Art Unit	2134
Examiner Name	Tran, Ellen

Attorney Docket No: 42P08112

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	EP-EP0892521	01/20/1999	Zamek, Steven			
	EP-EP1055989	11/29/2000	Proudlar, Graeme J., et al.			
	EP-EP1056014	11/29/2000	Proudlar, Graeme J., et al.			
	WO-WO0201794	01/03/2002	Ellison, Carl			
	WO-WO02060121	08/01/2002	Grawrock, David W.			
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	WO-WO9918511	04/15/1999	Edrich, David R.			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ECT		COULOURIS, GEORGE, et al., "Distributed Systems, Concepts and Designs", 2nd Edition, (1994), 422-424	
		CRAWFORD, JOHN, "Architecture of the Intel 80386", <u>Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86)</u> , (October 6, 1986), 155-160	
		FABRY, R.S., "Capability-Based Addressing", <u>Fabry, R.S., "Capability-Based Addressing," Communications of the ACM, Vol. 17, No. 7, (July 1974), 403-412</u>	
		FRIEDER, GIDEON, "The Architecture And Operational Characteristics of the VMX Host Machine", <u>The Architecture And Operational Characteristics of the VMX Host Machine, IEEE, (1982), 9-16</u>	
		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", <u>Intel Product Specification, Order Number 245359-001, (01/2000), 1-112</u>	
		INTEL CORPORATION, "Intel IA-64 Architecture Software Developer's Manual", <u>Volume 2: IA-64 System Architecture, Order Number 245318-001, (01/2000), i, ii, 5.1-5.3, 11.1-11.8, 11.23-11.26</u>	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press Series on Discrete Mathematics and its Applications, Boca Raton, FL, XP002165287, ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570</u>	
		NANBA, S., et al., "VM/4: ACOS-4 Virtual Machine Architecture", <u>VM/4: ACOS-4 Virtual Machine Architecture, IEEE, (1985), 171-178</u>	
		RSA SECURITY, "Hardware Authenticators", <u>www.rsasecurity.com/node.asp?id=1158, 1-2</u>	
		RSA SECURITY, "RSA SecurID Authenticators", <u>www.rsasecurity.com/products/secrid/datasheets/SID_DS_0103.pdf, 1-2</u>	
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EXAMINER

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10 Nov '04

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Application Number	09/540,611
Filing Date	March 31, 2000
First Named Inventor	Ellison, Carl
Group Art Unit	2134
Examiner Name	Tran, Ellen

Sheet 2 of 2

Attorney Docket No: 42P08112

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

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ECF		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995),47-52	
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		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP0021111449; ISBN 0471117099,(Oct. 1995),169-187	
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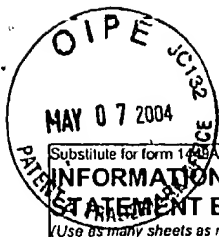
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STATEMENT BY APPLICANT**
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Complete if Known

Application Number	09/540611
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First Named Inventor	Ellison, Carl
Group Art Unit	Unknown
Examiner Name	Unknown

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Sheet 1 of 1

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